

### **REMARKS**

Claims 1-18 are pending in this application.

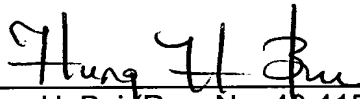
Claims 1-18 are believed to be allowable over the prior art of record for reasons that none of the cited prior art of record discloses the features of "a memory controller" as shown in FIGs. 22-26, including, for example, "a CPU interface arranged to interface with a CPU; a memory interface arranged to interface with a memory storing programs to be processed in the CPU and display data to be displayed on a display; a display control circuit to control output of display data to the display; a rendering process circuit to access the memory; and a memory control circuit to control access to the memory from one of the CPU, the display controller and the rendering processor," wherein, "when an access to the memory from the CPU is requested at the time when the rendering process circuit accesses the memory, the memory control circuit stops the access to the memory from the rendering process circuit until the access to the memory from the CPU ends, and when the access to the memory from the CPU ends, resumes the access to the memory from the rendering process circuit from the beginning" as defined in each of Applicants' base claims 1, 7 and 13.

In view of the foregoing additions and remarks, all claims 1-18 are deemed in condition for examination and allowance. Should any questions remain unresolved, the Examiner is requested to telephone Applicants' attorney at (703) 312-6600.

No fee is included for this Preliminary Amendment. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, or credit any overpayment of fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (503.35686CX3).

Respectfully submitted,

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